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PATENT ABSTRACTS OF JAPAN

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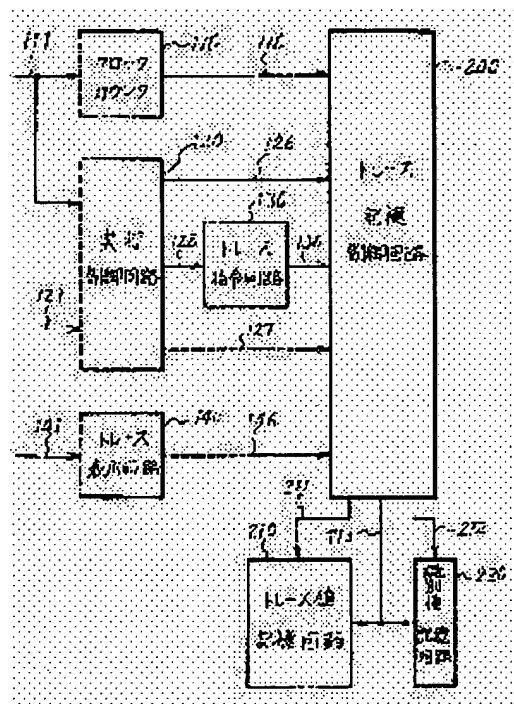
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(54) INSTRUCTION TRACING DEVICE

(57)Abstract:

PURPOSE: To efficiently use a trace storing area without storing an instruction address and an instruction word as trace information during the sequential execution of instructions.

CONSTITUTION: This instruction tracing device is provided with a clock counter 110 for counting up a clock signal, an execution control circuit 120 for generating timing for outputting an instruction address in executing while executing the instruction and sampling a trace as necessary, a trace command circuit 130 for commanding the storage of a branched instruction address at the time of generating a branch, a trace display circuit 140 for displaying the execution of tracing operation, a trace value storing circuit 210 for storing a trace value, an identification value storing circuit 220 for storing the sort of a corresponding trace value, and a trace storage control circuit 200 for controlling an access to the circuits 210, 220.



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